

DATA SHEET

BF990A

N-channel dual-gate MOS-FET

Product specification
File under Discrete Semiconductors, SC07

April 1991

N-channel dual-gate MOS-FET

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FEATURES

- Protected against excessive input voltage surges by integrated back-to-back diodes between gates and source.

APPLICATIONS

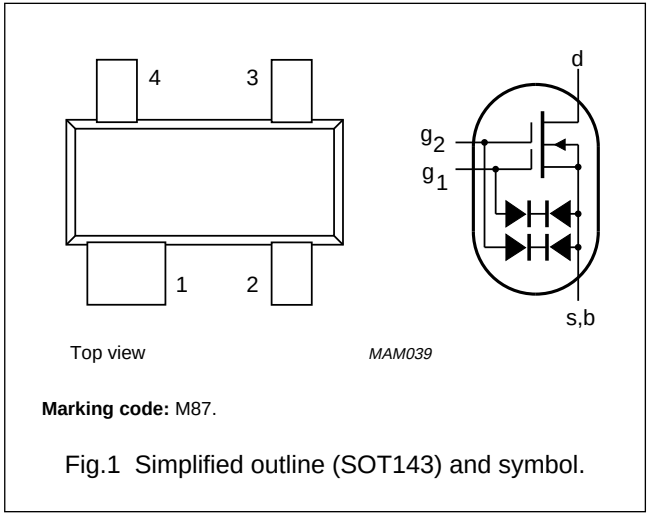
- RF applications such as:
 - Television tuners with 12 V supply voltage
 - Professional communication equipment.

PINNING

PIN	SYMBOL	DESCRIPTION
1	s, b	source
2	d	drain
3	g ₂	gate 2
4	g ₁	gate 1

DESCRIPTION

Depletion type field-effect transistor in a plastic SOT143 microminiature package with interconnected source and substrate.



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V _{DS}	drain-source voltage		–	18	V
I _D	drain current		–	30	mA
P _{tot}	total power dissipation	up to T _{amb} = 60 °C	–	200	mW
T _j	junction temperature		–	150	°C
Y _{fs}	transfer admittance	f = 1 kHz; I _D = 10 mA; V _{DS} = 10 V; V _{G2-S} = 4 V	19	–	mS
C _{ig1-s}	input capacitance at gate 1	f = 1 MHz; I _D = 10 mA; V _{DS} = 10 V; V _{G2-S} = 4 V	2.6	3	pF
C _{rs}	feedback capacitance	f = 1 MHz; I _D = 10 mA; V _{DS} = 10 V; V _{G2-S} = 4 V	25	–	fF
F	noise figure	f = 800 MHz; G _S = 5 mS; B _S = B _{Sopt} ; I _D = 10 mA; V _{DS} = 10 V; V _{G2-S} = 4 V	2	3	dB

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LIMITING VALUES

In according with the Absolute Maximum Rating System (IEC 134).

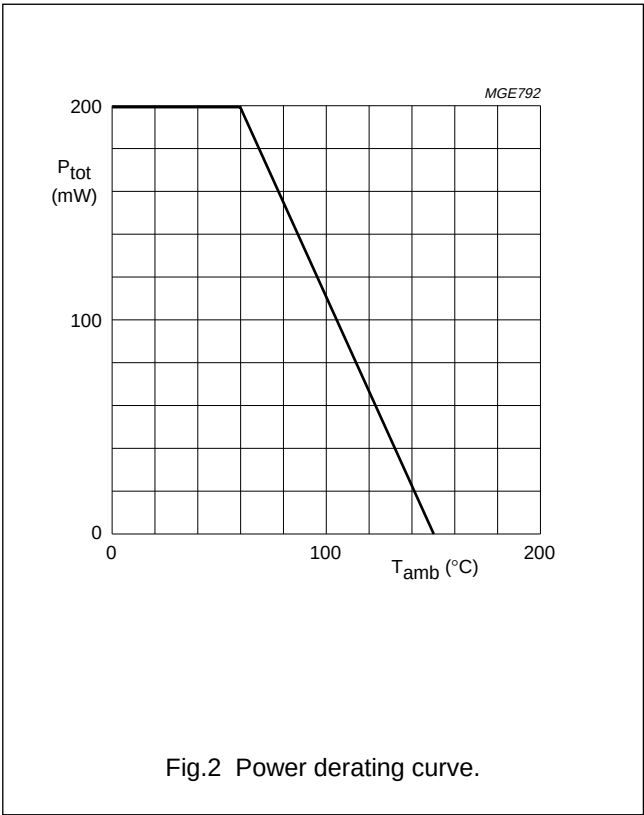
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{DS}	drain-source voltage		–	18	V
I _D	drain current (DC)		–	30	mA
I _{G1-S}	gate 1-source current		–	±10	mA
I _{G2-S}	gate 2-source current		–	±10	mA
P _{tot}	total power dissipation	up to T _{amb} = 60 °C; note 1	–	200	mW
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	in free air; note 1	460	K/W

Note to the Limiting values and the Thermal characteristics

1. Device mounted on a ceramic substrate of 8 × 10 × 0.7 mm.



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STATIC CHARACTERISTICS

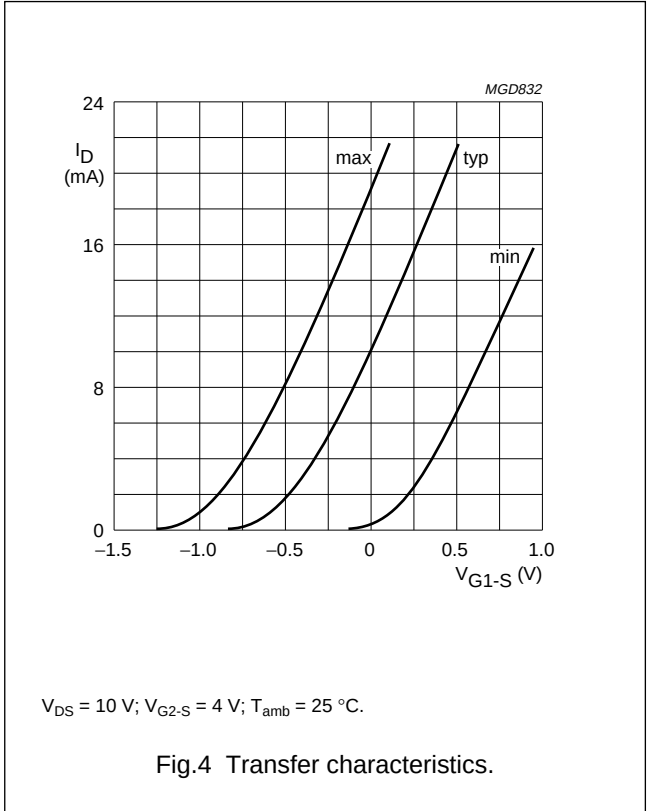
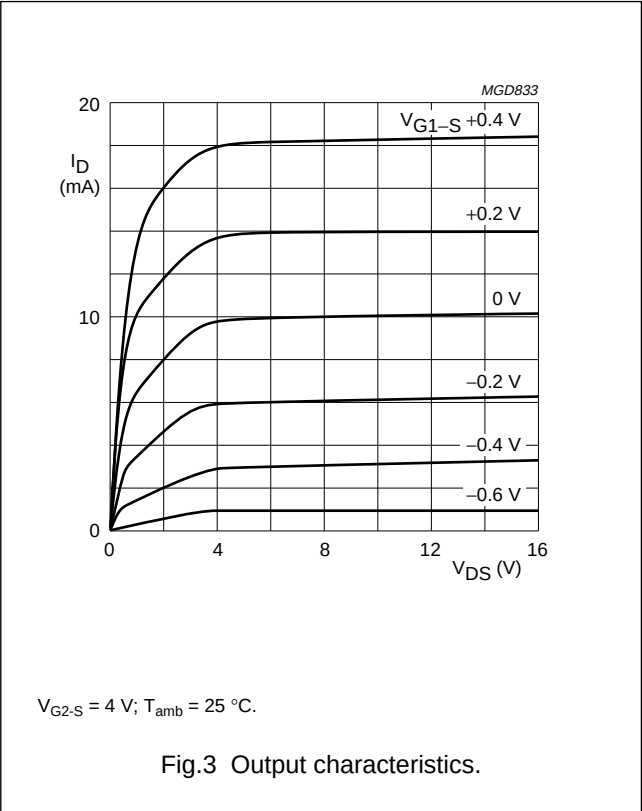
T_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{G1-SS}	gate 1 cut-off current	V _{G1-S} = ±7 V; V _{G2-S} = V _{DS} = 0	–	±25	nA
I _{G2-SS}	gate 2 cut-off current	V _{G2-S} = ±7 V; V _{G1-S} = V _{DS} = 0	–	±25	nA
V _{(BR)G1-SS}	gate 1-source breakdown voltage	I _{G1-SS} = ±10 mA; V _{G2-S} = V _{DS} = 0	±8	±20	V
V _{(BR)G2-SS}	gate 2-source breakdown voltage	I _{G2-SS} = ±10 mA; V _{G1-S} = V _{DS} = 0	±8	±20	V
V _{(P)G1-S}	gate 1-source cut-off voltage	I _D = 20 µA; V _{DS} = 10 V; V _{G2-S} = 4 V	–	–1.3	V
V _{(P)G2-S}	gate 2-source cut-off voltage	I _D = 20 µA; V _{DS} = 10 V; V _{G1-S} = 0	–	–1.1	V

DYNAMIC CHARACTERISTICS

Measuring conditions (common source): I_D = 10 mA; V_{DS} = 10 V; V_{G2-S} = 4 V; T_{amb} = 25 °C.

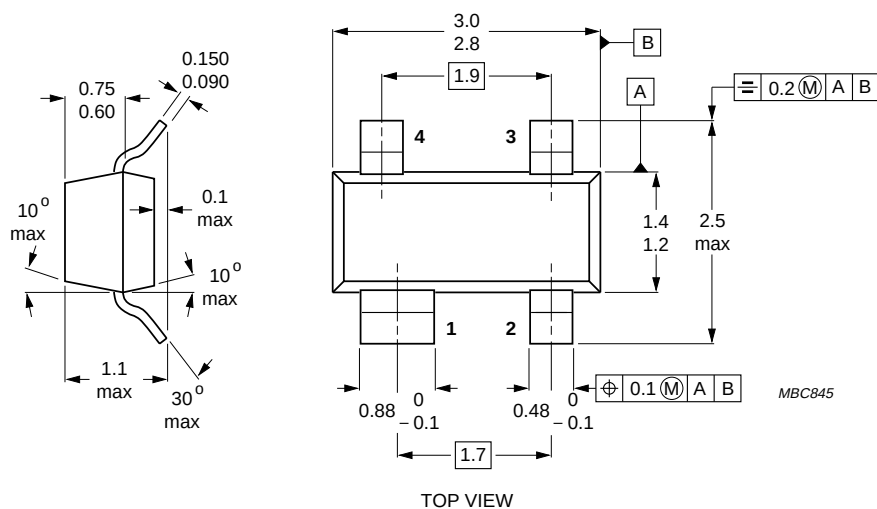
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Y _{fs}	transfer admittance	f = 1 kHz	18	19	–	mS
C _{ig1-s}	input capacitance at gate 1	f = 1 MHz	–	2.6	3	pF
C _{ig2-s}	input capacitance at gate 2	f = 1 MHz	–	1.4	–	pF
C _{rs}	feedback capacitance	f = 1 MHz	–	25	–	fF
C _{os}	output capacitance	f = 1 MHz	–	1.2	–	pF
F	noise figure	f = 800 MHz; G _S = 5 mS; B _S = B _{Sopt}	–	2	3	dB



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PACKAGE OUTLINE



Dimensions in mm.

See also "Soldering recommendations".

Fig.5 SOT143.

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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NOTES

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